

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	2	"20020053063".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:29
L8	7818	circuit same design same specification	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:36
L9	80	map\$4 adj20 (technolog\$3 near9 level)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:36
L10	7013	layout same technolog\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:37
L11	16	I8 and I9 and I10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:37
L12	0	cmos and I11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:51
L13	19367	current near9 sink\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 14:11
L14	0	I7 and I13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:52
L15	17	Bhattacharya near5 Debashis	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 14:12

L16	8	alter\$5 and I15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 14:13
L17	3	alter\$5 and I15 and I10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 14:13